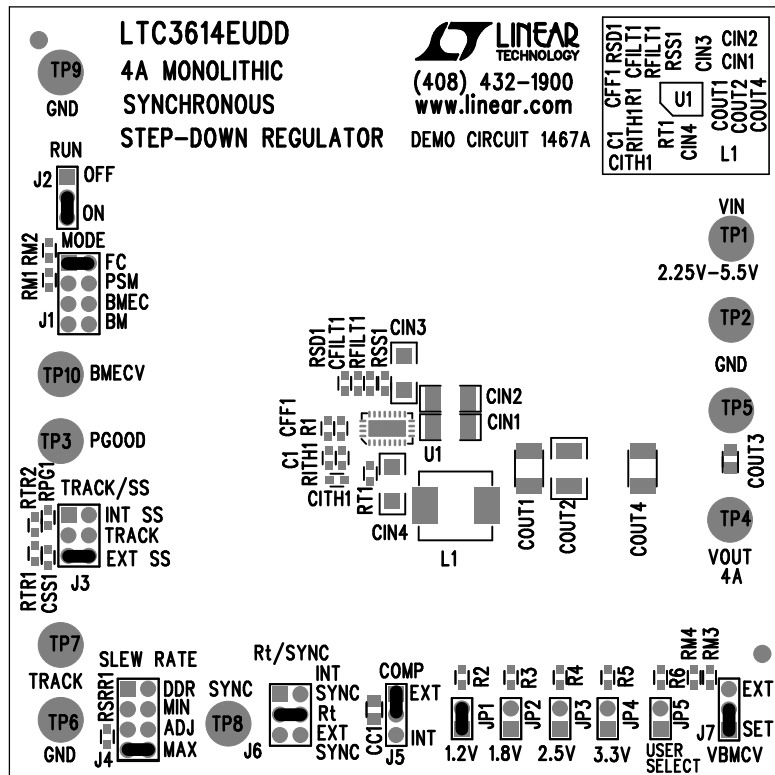
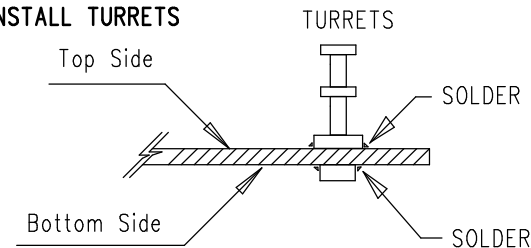


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	TOM G.	03-11-10

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY PROCESS SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIAL. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
J1 : FC J3 : EXTSS J5 : EXT J7 : SET
J2 : ON J4 : MAX J6 : Rt JP1 : 1.2V
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. DO NOT APPLY ANY KIND OF ASSEMBLY STAMP OR QA STAMP TO ANY BOARD.
7. INSTALL TURRETS



Silkscreen Top

LINEAR TECHNOLOGY
DC1467A-LTC3614EUDD
4A MONOLITHIC SYNCHRONOUS
STEP-DOWN REGULATOR
DATE: 03-11-10

APPROVALS

PCB DES. AK

APP ENG. TOM G.

SCALE = NONE

LINEAR TECHNOLOGY

1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
www.linear.com
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING
4A MONOLITHIC SYNCHRONOUS
STEP-DOWN REGULATOR

SIZE N/A IC NO. LTC3614EUDD
DEMO CIRCUIT 1467A

REV. 1

FILENAME: DC1467A-1.PCB

SHT 1 OF 1